

Z80ニモニック機械語対応表

2byte 3byte 4byte

n: 1バイト aa/nn: 2バイト(アドレスなど)

e: アドレス相対値(1バイト、-128～+127)

d: アドレス変位量(1バイト、0～255)

	00*****				01*****				10*****				11*****			
	0*	1*	2*	3*	4*	5*	6*	7*	8*	9*	A*	B*	C*	D*	E*	F*
0	NOP	DJNZ e	JR NZ,e	JR NC,e	LD B,B	LD D,B	LD H,B	LD (HL),B	ADD A,B	SUB B	AND B	OR B	RET NZ	RET NC	RET PO	RET P
1	LD BC,nn	LD DE,nn	LD HL,nn	LD SP,nn	LD B,C	LD D,C	LD H,C	LD (HL),C	ADD A,C	SUB C	AND C	OR C	POP BC	POP DE	POP HL	POP AF
2	LD (BC),A	LD (DE),A	LD (aa),HL	LD (aa),A	LD B,D	LD D,D	LD H,D	LD (HL),D	ADD A,D	SUB D	AND D	OR D	JP NZ,aa	JP NC,aa	JP PO,aa	JP P,aa
3	INC BC	INC DE	INC HL	INC SP	LD B,E	LD D,E	LD H,E	LD (HL),E	ADD A,E	SUB E	AND E	OR E	JP aa	OUT (n),A	EX (SP),HL	DI
4	INC B	INC D	INC H	INC (HL)	LD B,H	LD D,H	LD H,H	LD (HL),H	ADD A,H	SUB H	AND H	OR H	CALL NZ,aa	CALL NC,aa	CALL PO,aa	CALL P,aa
5	DEC B	DEC D	DEC H	DEC (HL)	LD B,L	LD D,L	LD H,L	LD (HL),L	ADD A,L	SUB L	AND L	OR L	PUSH BC	PUSH DE	PUSH HL	PUSH AF
6	LD B,n	LD D,n	LD H,n	LD (HL),n	LD B,(HL)	LD D,(HL)	LD H,(HL)	HALT	ADD A,(HL)	SUB (HL)	AND (HL)	OR (HL)	ADD A,n	SUB n	AND n	OR n
7	RLCA	RLA	DAA	SCF	LD B,A	LD D,A	LD H,A	LD (HL),A	ADD A,A	SUB A	AND A	OR A	RST 00H	RST 10H	RST 20H	RST 30H
8	EX AF,AF	JR e	JR Z,e	JR C,e	LD C,B	LD E,B	LD L,B	LD A,B	ADC A,B	SBC A,B	XOR B	CP B	RET Z	RET C	RET PE	RET M
9	ADD HL,BC	ADD HL,DE	ADD HL,HL	ADD HL,SP	LD C,C	LD E,C	LD L,C	LD A,C	ADC A,C	SBC A,C	XOR C	CP C	RET	EXX	JP (HL)	LD SP,HL
A	LD A,(BC)	LD A,(DE)	LD HL,(aa)	LD A,(aa)	LD C,D	LD E,D	LD L,D	LD A,D	ADC A,D	SBC A,D	XOR D	CP D	JP Z,aa	JP C,aa	JP PE,aa	JP M,aa
B	DEC BC	DEC DE	DEC HL	DEC SP	LD C,E	LD E,E	LD L,E	LD A,E	ADC A,E	SBC A,E	XOR E	CP E	⇒CB	IN A,(n)	EX DE,HL	EI
C	INC C	INC E	INC L	INC A	LD C,H	LD E,H	LD L,H	LD A,H	ADC A,H	SBC A,H	XOR H	CP H	CALL Z,aa	CALL C,aa	CALL PE,aa	CALL M,aa
D	DEC C	DEC E	DEC L	DEC A	LD C,H	LD E,L	LD L,L	LD A,L	ADC A,L	SBC A,H	XOR L	CP L	CALL aa	⇒DD	⇒ED	⇒FD
E	LD C,n	LD E,n	LD L,n	LD A,n	LD C,(HL)	LD E,(HL)	LD L,(HL)	LD A,(HL)	ADC A,(HL)	SBC A,(HL)	XOR (HL)	CP (HL)	ADC A,n	SBC A,n	XOR n	CP n
F	RRA	RRA	CPL	CCF	LD C,A	LD E,A	LD L,A	LD A,A	ADC A,A	SBC A,A	XOR A	CP A	RST 08H	RST 18H	RST 28H	RST 38H

CB	00	1*	2*	3*	4*	5*	6*	7*	8*	9*	A*	B*	C*	D*	E*	F*
0	RLC B	RL B	SLA B		BIT 0,B	BIT 2,B	BIT 4,B	BIT 6,B	RES 0,B	RES 2,B	RES 4,B	RES 6,B	SET 0,B	SET 2,B	SET 4,B	SET 6,B
1	RLC C	RL C	SLA C		BIT 0,C	BIT 2,C	BIT 4,C	BIT 6,C	RES 0,C	RES 2,C	RES 4,C	RES 6,C	SET 0,C	SET 2,C	SET 4,C	SET 6,C
2	RLC D	RL D	SLA D		BIT 0,D	BIT 2,D	BIT 4,D	BIT 6,D	RES 0,D	RES 2,D	RES 4,D	RES 6,D	SET 0,D	SET 2,D	SET 4,D	SET 6,D
3	RLC E	RL E	SLA E		BIT 0,E	BIT 2,E	BIT 4,E	BIT 6,E	RES 0,E	RES 2,E	RES 4,E	RES 6,E	SET 0,E	SET 2,E	SET 4,E	SET 6,E
4	RLC H	RL H	SLA H		BIT 0,H	BIT 2,H	BIT 4,H	BIT 6,H	RES 0,H	RES 2,H	RES 4,H	RES 6,H	SET 0,H	SET 2,H	SET 4,H	SET 6,H
5	RLC L	RL L	SLA L		BIT 0,L	BIT 2,L	BIT 4,L	BIT 6,L	RES 0,L	RES 2,L	RES 4,L	RES 6,L	SET 0,L	SET 2,L	SET 4,L	SET 6,L
6	RLC (HL)	RL (HL)	SLA (HL)		BIT 0,(HL)	BIT 2,(HL)	BIT 4,(HL)	BIT 6,(HL)	RES 0,(HL)	RES 2,(HL)	RES 4,(HL)	RES 6,(HL)	SET 0,(HL)	SET 2,(HL)	SET 4,(HL)	SET 6,(HL)
7	RLC A	RL A	SLA A		BIT 0,A	BIT 2,A	BIT 4,A	BIT 6,A	RES 0,A	RES 2,A	RES 4,A	RES 6,A	SET 0,A	SET 2,A	SET 4,A	SET 6,A
8	RRC B	RR B	SRA B	SRL B	BIT 1,B	BIT 3,B	BIT 5,B	BIT 7,B	RES 1,B	RES 3,B	RES 5,B	RES 7,B	RES 1,B	RES 3,B	RES 5,B	RES 7,B
9	RRC C	RR C	SRA C	SRL C	BIT 1,C	BIT 3,C	BIT 5,C	BIT 7,C	RES 1,C	RES 3,C	RES 5,C	RES 7,C	RES 1,C	RES 3,C	RES 5,C	RES 7,C
A	RRC D	RR D	SRA D	SRL D	BIT 1,D	BIT 3,D	BIT 5,D	BIT 7,D	RES 1,D	RES 3,D	RES 5,D	RES 7,D	RES 1,D	RES 3,D	RES 5,D	RES 7,D
B	RRC E	RR E	SRA E	SRL E	BIT 1,E	BIT 3,E	BIT 5,E	BIT 7,E	RES 1,E	RES 3,E	RES 5,E	RES 7,E	RES 1,E	RES 3,E	RES 5,E	RES 7,E
C	RRC H	RR H	SRA H	SRL H	BIT 1,H	BIT 3,H	BIT 5,H	BIT 7,H	RES 1,H	RES 3,H	RES 5,H	RES 7,H	RES 1,H	RES 3,H	RES 5,H	RES 7,H
D	RRC L	RR L	SRA L	SRL L	BIT 1,L	BIT 3,L	BIT 5,L	BIT 7,L	RES 1,L	RES 3,L	RES 5,L	RES 7,L	RES 1,L	RES 3,L	RES 5,L	RES 7,L
E	RRC (HL)	RR (HL)	SRA (HL)	SRL (HL)	BIT 1,(HL)	BIT 3,(HL)	BIT 5,(HL)	BIT 7,(HL)	RES 1,(HL)	RES 3,(HL)	RES 5,(HL)	RES 7,(HL)	RES 1,(HL)	RES 3,(HL)	RES 5,(HL)	RES 7,(HL)
F	RRC A	RR A	SRA A	SRL A	BIT 1,A	BIT 3,A	BIT 5,A	BIT 7,A	RES 1,A	RES 3,A	RES 5,A	RES 7,A	RES 1,A	RES 3,A	RES 5,A	RES 7,A

DD	00	1*	2*	3*	4*	5*	6*	7*	8*	9*	A*	B*	C*	D*	E*	F*
0								LD (IX+d),B								
1			LD IX,aa					LD (IX+d),C							POP IX	
2			LD (aa),IX					LD (IX+d),D								
3			INC IX					LD (IX+d),E							EX (SP),IX	
4				INC (IX+d)				LD (IX+d),H								
5				DEC (IX+d)				LD (IX+d),L							PUSH IX	
6			LD (IX+d),n	LD B,(IX+d)	LD D,(IX+d)	LD H,(IX+d)		ADD A,(IX+d)	SUB (IX+d)	AND (IX+d)	OR (IX+d)					
7								LD (IX+d),A								
8	ADD IX,BC	ADD IX,DE	ADD IX,IX	ADD IX,SP											JP (IX)	LD SP,IX
A			LD IX,(aa)													
B			DEC IX										⇒CB2			
E					LD C,(IX+d)	LD E,(IX+d)	LD L,(IX+d)	LD A,(IX+d)	ADC A,(IX+d)	SBC A,(IX+d)	XOR (IX+d)	CP (IX+d)				

CB2	00	1*	2*	3*	4*	5*	6*	7*	8*	9*	A*	B*	C*	D*	E*	F*
6	RLC (IX+d)	RL (IX+d)	SLA (IX+d)		BIT 0,(IX+d)	BIT 2,(IX+d)	BIT 4,(IX+d)	BIT 6,(IX+d)	RES 0,(IX+d)	RES 2,(IX+d)	RES 4,(IX+d)	RES 6,(IX+d)	SET 0,(IX+d)	SET 2,(IX+d)	SET 4,(IX+d)	SET 6,(IX+d)
E	RRC (IX+d)	RR (IX+d)	SRA (IX+d)	SRL (IX+d)	BIT 1,(IX+d)	BIT 3,(IX+d)	BIT 5,(IX+d)	BIT 7,(IX+d)	RES 1,(IX+d)	RES 3,(IX+d)	RES 5,(IX+d)	RES 7,(IX+d)	SET 1,(IX+d)	SET 3,(IX+d)	SET 5,(IX+d)	SET 7,(IX+d)

ED	00	1*	2*	3*	4*	5*	6*	7*	8*	9*	A*	B*	C*	D*	E*	F*
0					IN B,(C)	IN D,(C)	IN H,(C)				LDI	LDIR				
1					OUT (C),B	OUT (C),C	OUT (C),H				CPI	CPIR				
2					SBC HL,BC	SBC HL,DE	SBC HL,HL	SBC HL,SP			INI	INIR				
3					LD (aa),BC	LD (aa),DE		LD (aa),SP			OUTI	OTIR				
4					NEG											
5					RETN											
6					IM 0	IM 1										
7					LD IA	LD A,I	RDD									
8					IN C,(C)	IN E,(C)	IN L,(C)	IN A,(C)			LDD	LDDR				
9					OUT (C),C	OUT (C),E	OUT (C),L	OUT (C),A			CPD	CPDR				
A					ADC HL,BC	ADC HL,DE	ADC HL,HL	ADC HL,SP			IND	INDR				
B					LD BC,(aa)	LD DE,(aa)		LD SP,(aa)			OUTD	OTDR				
D					RETI											
F						IM 2										
					LD R,A	LD A,R	RLD									

FD	00	1*	2*	3*	4*	5*	6*	7*	8*	9*	A*	B*	C*	D*	E*	F*
0								LD (IY+d),B								
1			LD IY,aa					LD (IY+d),C							POP IY	
2			LD (aa),IY					LD (IY+d),D								
3			INC IY					LD (IY+d),E							EX (SP),IY	
4				INC (IY+d)				LD (IY+d),H								
5				DEC (IY+d)				LD (IY+d),L							PUSH IY	
6			LD (IY+d),n	LD B,(IY+d)	LD D,(IY+d)	LD H,(IY+d)		ADD A,(IY+d)	SUB (IY+d)	AND (IY+d)	OR (IY+d)					
7								LD (IY+d),A								
8	ADD IY,BC	ADD IY,DE	ADD IY,IY	ADD IY,SP											JP (IY)	LD SP,IY
A			LD IY,(aa)													
B			DEC IY										⇒CB3			
E					LD C,(IY+d)	LD E,(IY+d)	LD L,(IY+d)	LD A,(IY+d)	ADC A,(IY+d)	SBC A,(IY+d)	XOR (IY+d)	CP (IY+d)				

CB3	00	1*	2*	3*	4*	5*	6*	7*	8*	9*	A*	B*	C*	D*	E*	F*
6	RLC (Y+d)	RL (Y+d)	SLA (Y+d)		BIT 0,(Y+d)	BIT 2,(Y+d)	BIT 4,(Y+d)	BIT 6,(Y+d)	RES 0,(Y+d)	RES 2,(Y+d)	RES 4,(Y+d)	RES 6,(Y+d)	SET 0,(Y+d)	SET 2,(Y+d)	SET 4,(Y+d)	SET 6,(Y+d)
E	RRC (Y+d)	RR (Y+d)	SRA (Y+d)	SRL (Y+d)	BIT 1,(Y+d)	BIT 3,(Y+d)	BIT 5,(Y+d)	BIT 7,(Y+d)	RES 1,(Y+d)	RES 3,(Y+d)	RES 5,(Y+d)	RES 7,(Y+d)	SET 1,(Y+d)	SET 3,(Y+d)	SET 5,(Y+d)	SET 7,(Y+d)